

FDC6330L

Integrated Load Switch

General Description

This device is particularly suited for compact power management in portable electronic equipment where 3V to 20V input and 2.3A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOTTM-6 package.

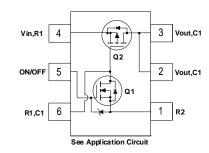
Applications

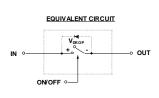
- Power management
- Load actuation

Features

- $\begin{array}{l} \bullet \text{ V} \\ V \\ V \\ DROP \end{array} = 0.2V \text{ @ V} \\ V \\ IN \\ IN \end{array} = 12V, \\ I \\ L \\ = 2.5 \text{ A. R} \\ IN \\ = 0.08 \text{ } \Omega \\ ON) \\ = 0.125 \text{ } \Omega. \end{array}$
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6kV Human Body Model).
- High performance PowerTrenchTM technology for extremely low on-resistance.
- SuperSOTTM-6 package design using copper lead frame for superior thermal and electrical capabilities.







SuperSOT[™]-6

Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{IN}	Input Voltage Range	(Note 1)	3 - 20	V
V _{ON/OFF}	On/Off Voltage Range		1.5 - 8	V
I _D	Load Current - Continuous	(Note 2)	2.3	A
	- Pulsed		10	
P _D	Maximum Power Dissipation	(Note 1)	0.7	W
T _J , T _{stg}	Operating and Storage Temperature R	ange	-55 to +150	°C
ESD	Electrostatic Discharge Rating MIL-ST Human-Body-Model (100pf/1500 Ohm)		6	kV

Thermal Characteristics

$R_{ heta^{JA}}$	Thermal Resistance, Junction-to-Ambient	(Note 2)	180	∘C/W
$R_{\theta^{JC}}$	Thermal Resistance, Junction-to-Case	(Note 2)	60	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
.330 (. Denotes pin 1)	FDC6330L	7"	8mm	3000 units

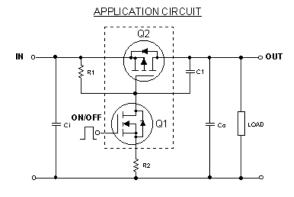
Electrical Characteristics TA=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF Ch	aracteristics					
I _{FL}	Leakage Current	$V_{IN} = 20 \text{ V}, V_{ON/OFF} = 250 \mu\text{A}$			1	μA
	aracteristics (Note 3)					
V_{DROP}	Conduction Voltage	$V_{IN} = 12 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_L = 2.5 \text{ A}$			0.2	V
V_{DROP}	Conduction Voltage	$V_{IN} = 12 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_L = 2.5 \text{ A}$ $V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_L = 1.6 \text{ A}$			0.2	V
	Conduction Voltage Q ₂ - Static On-Resistance	, , , , , , , , , , , , , , , , , ,		0.054 0.081	_	
V _{DROP} $R_{(ON)}$ I_L	, and the second	$V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_L = 1.6 \text{ A}$ $V_{GS} = -12 \text{ V}, I_D = -2.3 \text{ A}$	2.5		0.2	V

Notes

- 1. Range of V_{in} can be up to 30V, but R_1 and R_2 must be scaled such that V_{GS} of Q2 does not exceed 20V.
- 2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

FDC6330L Load Switch Application



External Component Recommendation:

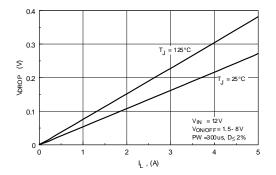
For applications where $Co \leq 1 \mu F.$

For slew rate control, select R2 in the range of 1k - $4.7 k\Omega$.

For additional in-rush current control, $C1 \le 1000 pF$ can be added.

Select R1 so that the R1/R2 ratio ranges from 10 - 100. R1 is required to turn Q2 off.

Typical Characteristics (continued)



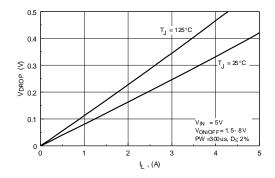


Figure 1. Conduction Voltage Drop Variation with Load Current.

Figure 2. Conduction Voltage Drop Variation with Load Current.

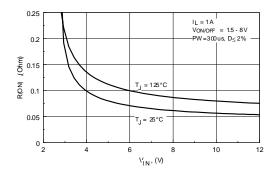


Figure 3. On-Resistance Variation with Input Voltage.

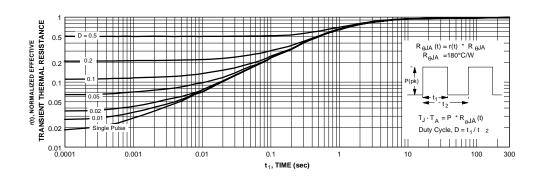
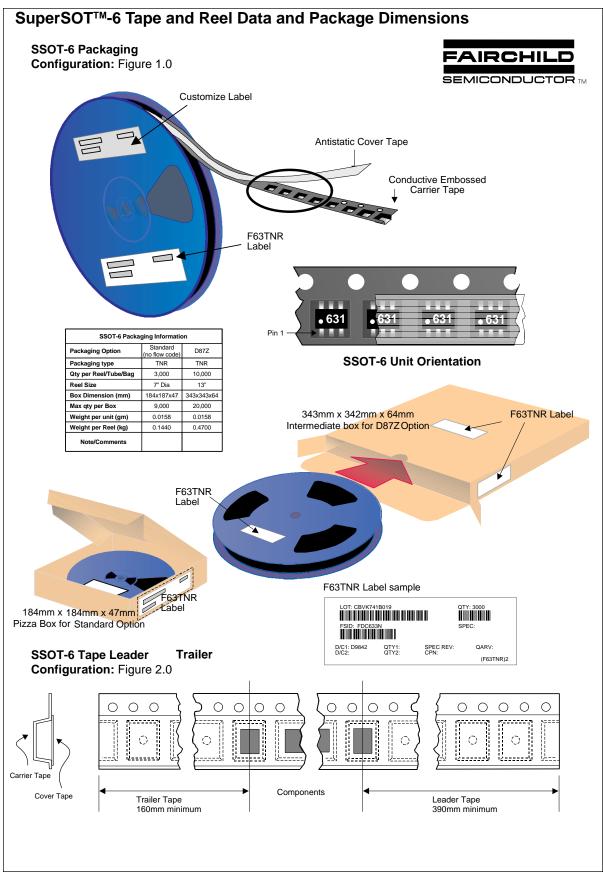
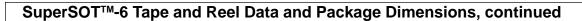


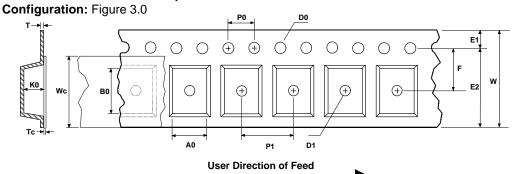
Figure 4. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 2. Transient themal response will change depending on the circuit board design.





SSOT-6 Embossed Carrier Tape

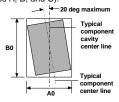


Dimensions are in millimeter														
Pkg type	A0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SSOT-6 (8mm)	3.23 +/-0.10	3.18 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.00 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.37 +/-0.10	0.255 +/-0.150	5.2 +/-0.3	0.06 +/-0.02

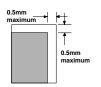
Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation

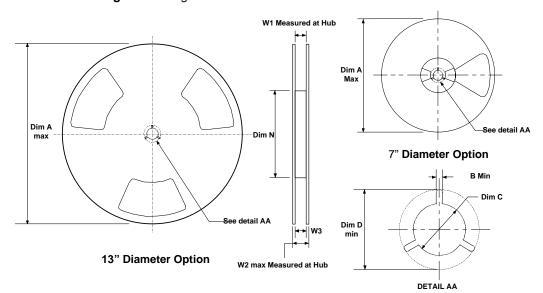


Sketch B (Top View)
Component Rotation



Sketch C (Top View)
Component lateral movement

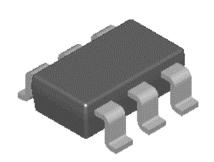
SSOT-6 Reel Configuration: Figure 4.0

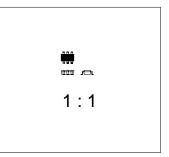


Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

SuperSOT[™]-6 Tape and Reel Data and Package Dimensions, continued

SuperSOT™-6 (FS PKG Code 31, 33)

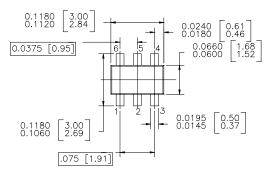


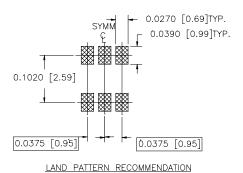


Scale 1:1 on letter size paper

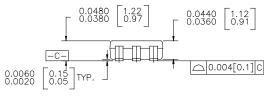
Dimensions shown below are in: inches [millimeters]

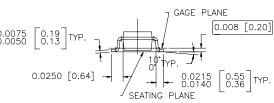
Part Weight per unit (gram): 0.0158





CONTROLLING DIMENSION IS INCH VALUES IN [] ARE MILLIMETERS





NOTES: UNLESS OTHERWISE SPECIFIED

1.0 STANDARD LEAD FINISH: 150 MICROINCHES 93.81 MICROMETERS) MINIMUM TIN / LEAD (SOLDER) ON COPPER.

2.0 NO JEDEC REGISTRATION AS OF JULY 1996

SUPER SOT 6 LEADS

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